

EXAMINER'S AMENDMENT

An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Thomas Catale at 408 938 9060 on 7/18/08.

The application has been amended as follows:

Claims 19 and 20 have been amended as follow:

19. (Currently Amended) ~~[A computer usable medium having computer-readable program code embodied therein]~~ A method for causing a memory device to provide the functionality of a dual-ported memory using single-ported memory for multiple frequencies, the method comprising:

receiving information at a first and second ports that are associated respectively with a first and second port synchronization logic devices;

communicating the information from the first and second ports, respectively, to the first and second port synchronization logic devices;

synchronizing the information, ~~[between the port]~~ between port frequencies and a core frequency associated with a single-ported memory core, at the first and the second port synchronization logic devices;

communicating the information from the first and second port synchronization logic devices to a port multiplexing logic that acts as a time division multiplexer (TDM) for the information; and

communicating the information from the TDM to the single-ported memory core.

20. (Currently Amended) [A computerusable medium] A method as described in Claim 19, wherein the core frequency associated with the single-ported memory core is at least twice that of the fastest [a bus for communicating the information between the port multiplexing logic and the single-ported memory core is at least twice the bandwidth of the highest] of the port frequencies.

The following is an examiner's statement of reasons for allowance:

Regarding claims 1, 12 and 19, the prior art does not disclose wherein the port multiplexing logic acts as a time division multiplexer (TDM) for the information communicated between the first and second port synchronization logic devices and the single-ported memory core in combination with other limitations in the claims.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Hanh Nguyen whose telephone number is 571 272 3092. The examiner can normally be reached on Monday-Thursday 8:30 AM - 4:30PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Lynn Feild can be reached on 571 272 2092. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/Hanh Nguyen/
Primary Examiner, Art Unit 2616